HD35100
SOLID STATE HIGH POWER AMPLIFIER

FEATURES

Designed for EMC-EMI/RFI, Lab, and general comm applications
Ultra-Compact Small form factor, light weight, high power density
Class A/AB linear advanced technology design
Instantaneous ultra-wide bandwidth
Suitable for all single channel modulation standards
Built-in protection circuits
High reliability and ruggedness



ELECTRICAL SPECIFICATIONS : 25°C, 50Ω

Parameter	Specification	Notes
Operating Frequency Range	20 - 3000 MHz	
Power Output @ Psat	10 Watt Min	CW
Power Gain	40 dB Min	0dBm or less for rated Pout
Power Gain Flatness	4.0 dB p-p Max	Constant input power
Input Return Loss	-10 dB Max	
2-Tone Intermodulation (IMD)	-30 dBc Typ	30dBm/Tone, Δ = 1MHz
Harmonics 2^{nd}/ 3^{rd}	2nd -12dBc Typ / 3rd -20dBc Typ	At rated Pout
Spurious	-60 dBc Max	Non-harmonics
Operating Voltage	28 - 30 VDC	
Current Consumption	2.5 Amp Max	At rated Pout
Input Power Protection	+8 dBm Max	<10 Sec without damage
Load VSWR Protection	∞ : 1	<1 Min at rated Pout

ENVIRONMENTAL CHARACTERISTICS

Parameter	Specification	Notes
Operating Case Temperature	-20 to +75 °C	
Storage Temperature	-40 to +85 °C	
Relative Humidity	up to 95 %	Non-condensing

MECHANICAL SPECIFICATIONS

Parameter	Specification	Notes
Dimensions	125 x 63 x 27 mm	Excluding connectors
Weight	300 gr.	Typical weight
RF Connectors In/Out	SMA female	
DC Power / Interface Connector	9-Pin D-Sub	
Cooling	External Heatsink	Forced air required

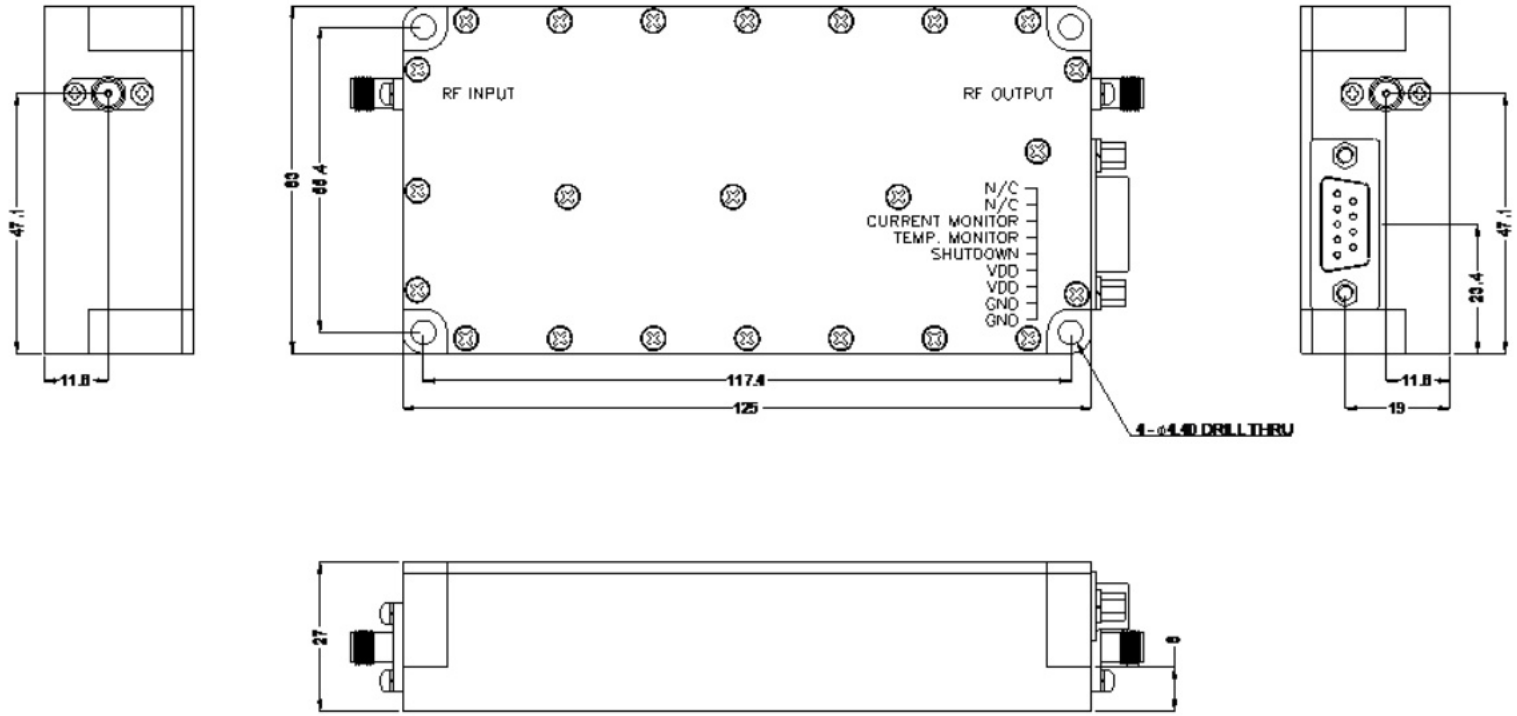
D-SUB CONNECTOR PIN ASSIGNMENT

Pin	Function	Description
1	N/C	N/C
2	VVA	Option-103 (With larger housing dimensions)
3	CURRENT SENSOR	ID@50mV/100mA Typ
4	TEMP SENSOR	V_T@10mV/°C + 500mV Typ
5	SHUTDOWN	Enable = TTL High or Open; Disable = TTL Low or Short
6, 7	VDD	28VDC
8, 9	GND	Ground

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Specifications subject to change without notice. Specifications and drawings may vary with selected options.

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OUTLINE DRAWING



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