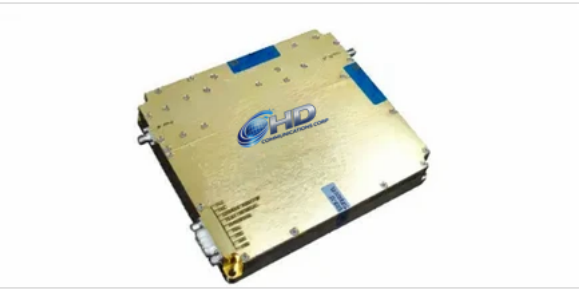
HD35103
SOLID STATE HIGH POWER AMPLIFIER

FEATURES

Class AB linear Advanced Technology Design
Instantaneous ultra-wide Bandwidth
State of the Art C&W in a small form factor housing
Suitable for all single channel modulation standards
Built-in protection circuits
High reliability and ruggedness



ELECTRICAL SPECIFICATIONS

Parameter	Specification	Notes
Operating Frequency Range	1.0 - 18.0 GHz	
Power Output Psat	10 Watt Min	Pin = -7dBm Typ for rated Pout
Power Gain	40 dB Min	
Power Gain Flatness	5 dB p-p Max	
Input Return Loss	10 dB Min	
Harmonics 2nd/3rd	-20 dBc Typ	-10~-15dBc Typ up to 2GHz
Spurious	-60 dBc Max	Non-harmonics
Operating Voltage	32 VDC Nom	
Current Consumption	5.0 Amp Max	At rated Pout
Input Power Protection	0 dBm Max	<10 Sec without damage
Load VSWR Protection	3 : 1 Max	<1 minute at rated Pout

ENVIRONMENTAL CHARACTERISTICS

Parameter	Specification	Notes
Operating Case Temperature	-20 to +65 °C	
Storage Temperature	-40 to +85 °C	
Relative Humidity	5 to 95 %	Non-condensing

MECHANICAL SPECIFICATIONS

Parameter	Specification	Notes
Dimensions	160 x 140 x 27 mm	Excluding connectors
Weight	1.4 Kg.	
RF Connectors In/Out	SMA female	Cover Flange
DC Power / Interface Connector	9-Pin D-Sub	
Cooling	External Heatsink	Forced air required

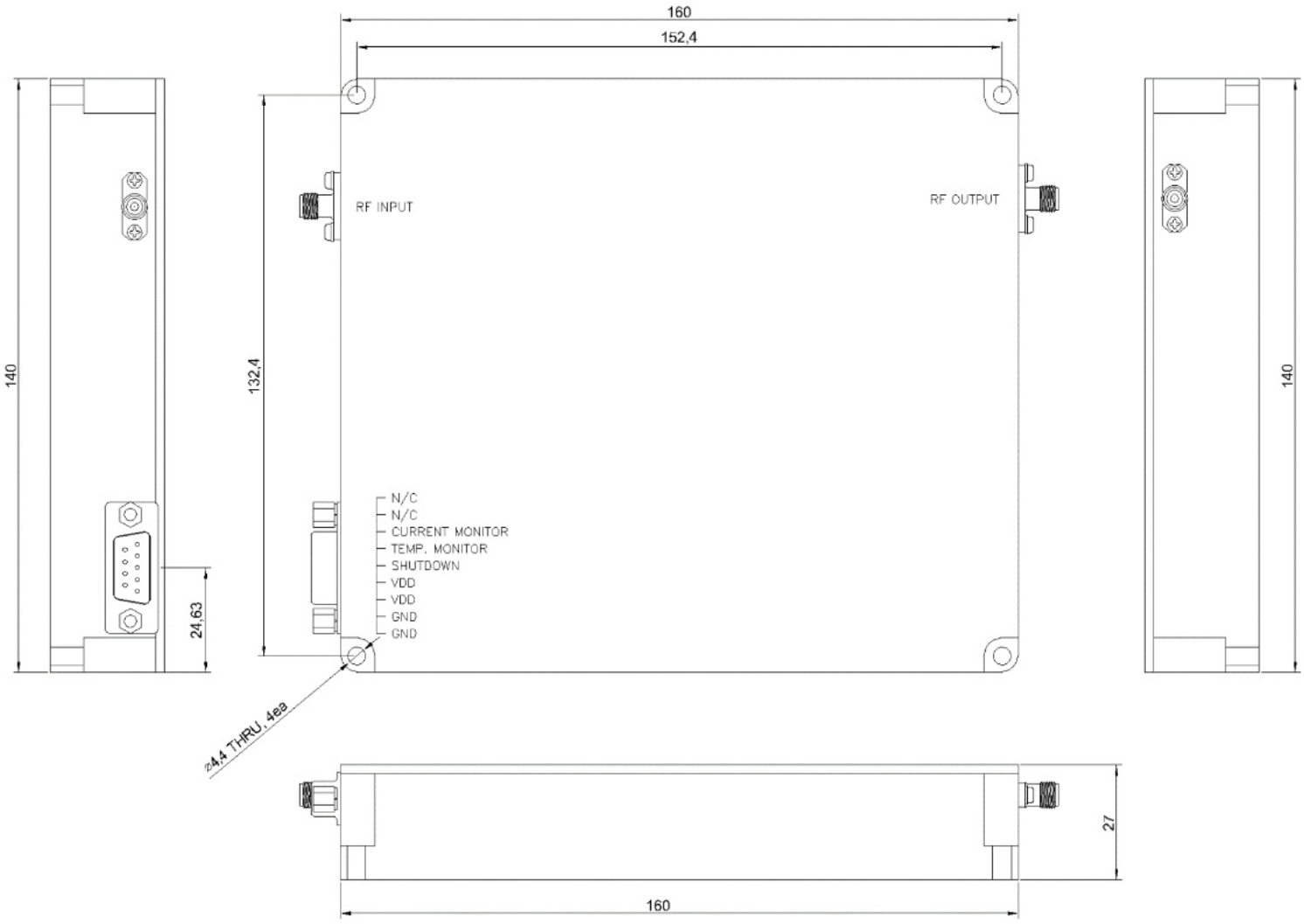
D-SUB CONNECTOR PIN ASSIGNMENT

Pin	Function	Description
1	N/A	N/A
2	N/A	N/A
3	CURRENT SENSOR	I_D@50mV/100mA Typ
4	TEMP SENSOR	V_T@10mV/°C + 500mV Typ
5	SHUTDOWN	Enable = TTL "High" or OPEN / Disable = TTL "Low" or GND
6,7	VDD	32VDC
8,9	GND	Ground

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Specifications subject to change without notice. Specifications and drawings may vary with selected options.

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OUTLINE DRAWING



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